PATENT ABSTRACTS OF JAPAN

(11)Publication number:

08-030517

(43) Date of publication of application: 02.02.1996

(51)Int.CI.

G06F 12/16 G06F 12/16 G11C 16/06 G11C 29/00

(21)Application number : 06-168465

(71)Applicant: MITSUBISHI ELECTRIC CORP

(22)Date of filing:

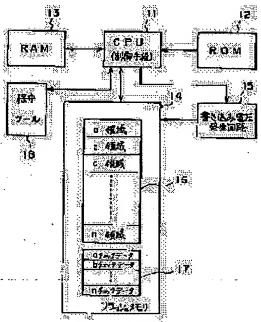
20.07.1994

(72)Inventor: SAKIMA NAOKI

(54) INSTRUMENTATION CONTROL DEVICE

(57)Abstract:

PURPOSE: To make it possible to independentely diagnose a flash memory without duplexing a CPU and preparing a shared memory by computing check data, and at the time of writing a program or the like to be changed in a data area, simultaneously writing the program or the like also in a corresponding check area. CONSTITUTION: When a change request for a program or data stored in a data area 16 of a flash memory 14 is generated from a maintenance tool 18, the changed data are temporarily stored in a RAM 13. Then check data for diagnosing the changed data in the area whose change is requested are computed and the computed result is stored in a RAM 13. Then a write voltage generating circuit 15 is instructed to generate write voltage for the memory 14, the contents of the corresponding area out of plural areas (a) to (n) in the data area 16 are rewritten... by the changed data stored in the RAM 13 and an area of a check area 17 corresponding to the changed area is rewritten by the check data stored in the RAM 13.



LEGAL STATUS

[Date of request for examination]

05.04.1996

[Date of sending the examiner's decision of

23.03.1999

rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's

decision of rejection]
[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office